GETTING STARTED:

1. If not previously installed, go to http://www.silabs.com/products/mcu/Pages/USBtoUARTBridgeVCPDrivers.aspx to download and install the Silicon Labs CP210x USB-to-UART driver.
2. Plug the UltraZed-EG SOM onto the IO Carrier Card via JX1/JX2/JX3 connectors.
3. Set the UltraZed-EG SOM SW2 Boot Mode switch (MODE[3:0] = SW2[4:1]) to ON, ON, ON, and OFF positions (Boot Mode set to QSPI, MODE[3:0] = 0x1).
5. Connect the USB A-mini-B cable to J11 on the IO Carrier Card and the USB port of the PC. This will provide USB-UART connection to the board.
6. Connect 12V power supply to J7 on the IO Carrier Card.
7. Use Device Manager to determine the COM Port. Open a terminal program, configure it to 115200/8/n/1/n.
8. Slide the SW8 power switch to the ON position on the IO Carrier Card.
9. The out-of-box design will run and you will see the following on the UART terminal (please allow time for Linux to boot). You should also see the PS on-board user Red LED flashing.

If you are using the UltraZed-EG SOM with ES1 silicon:
- Use the software voucher shipped with the kit to obtain and install Vivado with ES1 license.
- Create a text file called init.tcl and enter the following line: enable_beta_device*
- Save and close the init.tcl file.
- Place the init.tcl file in the \Xilinx\Vivado\2016.x\scripts folder.
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