

Avnet PicoZed 7010/7020 Revision B Errata

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1 Introduction

Thank you for your interest in the Avnet PicoZed System-On-Modules. Although Avnet has made every effort to ensure the highest possible quality, these kits and associated software are subject to the limitations described in this errata notification.

Be aware that any of the optional workarounds requiring physical modifications to the board are done at the User's own risk, and Avnet is not liable for poorly performed rework.

2 Identifying Affected Modules

The modules affected by these errata can be identified by the Revision of the PicoZed System-On-Module. The revision of the PicoZed System-On-Module can be found on the bottom side of the PCB board. The affected PCB boards are the PicoZed Revision B, identified as "**Z7PZ-Z70x0-PCB-B**". This affects the Revision B of the PicoZed 7010 or 7020 System-On-Modules.

3 Errata

3.1 Incorrect Footprint for Quad Multiplexer/ Demultiplexer

Applications Affected

Any application that would require the use of eMMC memory or the Zynq PS MIO pins that are routed to Pins 1 through 8 of the JX2 connector.

Description

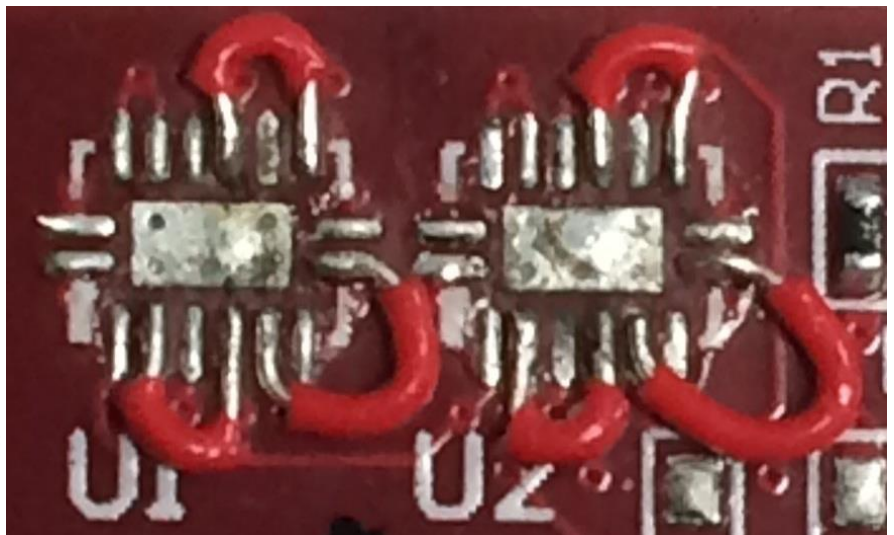
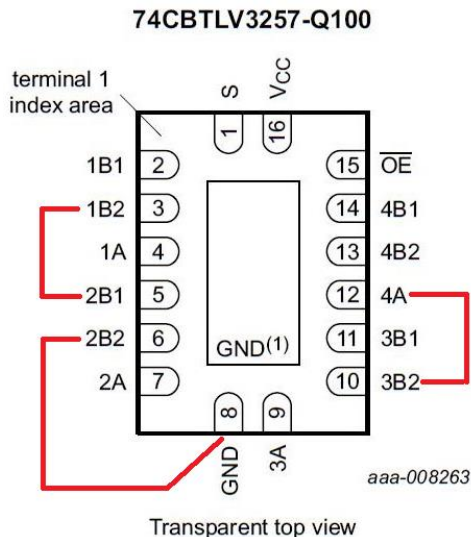
Quad Mux/Demux devices U1 and U2 on the 7010/7020 are affected by this erratum. The incorrect footprint renders the Mux/Demux circuit unusable and requires rework be performed to add in desired functionality.

NOTE:

All revision C boards have the correct footprint for these devices.

Workaround

Boards that have the incorrect footprint on them were reworked to install jumper wires to include the desired functionality. Avnet has chosen to make the eMMC device the default interface and has had boards reworked to ensure the functionality of the eMMC memory interface. The implementation of the eMMC interface via the jumper wires means the loss of the PS MIO interface at the JX2 connector for the Rev B boards. The customer can implement the JX2 interface if necessary by having the board optionally reworked to install jumper wires to implement the PS MIO pins to the JX2 connector. This will result in the loss of the eMMC memory interface.



4 New Erratum

Any new erratum found will be posted to the PicoZed website: www.picozed.org

5 Additional Support

For additional support, please review the discussions and post your questions to the PicoZed Forums at

<http://picozed.org/forums/picozed-hardware-design>

You can also contact your local Avnet/Silica FAE.

6 Revision History

Date	Version	Revision
11/10/2014	1.0	Initial Version, PicoZed Rev B