Subject: Modification required for TPS54618 EN voltage threshold

Products Affected: This PCN affects the part numbers listed below.

| AES-Z7PZ-7Z015-SOM-G | AES-Z7PZ-7Z030-C02-G | AES-Z7PZ-7Z030-SOM-I-G |
| AES-Z7PZ-7Z015-SOM-I-G | AES-Z7PZ-7Z030-SOM-G | AES-Z7PZ-SVDK-G |

Change Description: Remove R52 from the board.

Reason for Change: The PicoZed 7015 and 7030 modules use the Texas Instruments TPS54618 for core power. There is an error in the design where the ENABLE pin on this device is pulled up to Vin (5V) through a 10k resistor (R52). The maximum voltage on this pin is 4V. Exposing this pin to 5V may result in device damage where the EN pin develops a short to ground. This pin has an internal pullup, therefore the external pull-up is unnecessary. The TPS54618 EN pin is connected to the PWR_ENABLE signal on PicoZed, which is connected to JX1.pin5. The Avnet PicoZed FMC Carrier Card original as well as PicoZed FMC Carrier Card V2 both will drive this pin low when power is off or let it float when power is on.

Identifying Modified Boards: The labels on the back of the PicoZed PCB will denote the current revision as “C04” or later, BD-Z7PZP-Z7015-G REV C04 or BD-Z7PZP-Z7030-G REV C04

For customers in possession of boards of earlier revisions, please remove R52. Avnet will allow this single modification without affecting the status of the warranty.

For any questions regarding this PCN and/or if you are not comfortable making this modification please contact your local Avnet sales representative.

Additional information can be found @
http://zedboard.org/content/picozed-7015-7030-users-action-needed