

Product Change Notification PCN16006

PicoZed SDR 2x2 SOM Power Requirements Update

Subject: PicoZed SDR 2x2 System-on-Module (SOM) Power Requirements Update

Products Affected: This PCN affects the part numbers listed below.

AES-Z7PZ-SDR2-G
AES-Z7PZ-SDR2-DEV-G

Change Description:

- 1) Power sequencer firmware allows a system voltage input (JX_VIN) range of $5.0V \pm 10\%$ (4.50V – 5.50V)
- 2) Power sequencer firmware allows a system voltage input (JX_VIN) rise time window of 400ms
- 3) Power sequencer firmware requires only a single 5.0V supply (VIN) to successfully power up the module. In addition, the sequencer monitors all Zynq SoC user bank voltage rails and will power down the SOM if an overvoltage condition is detected

Reason for Change:

The PicoZed SDR 2x2 SOM uses the Analog Devices ADM1166 Super Sequencer to implement a complete supervisory and sequencing system to ensure all power rails are regulated properly. If the sequencer firmware on PicoZed SDR 2x2 SOMs detects a problem, it will disable all regulators to protect the SOM from damage. Early versions of the firmware on PicoZed SDR 2x2 SOMs prevented some carriers from successfully powering up the module. These changes to the ADM1166 firmware ensure reliable operation over a wider range of carrier designs

More Information

For a more detailed explanation about these changes, carrier voltage supply requirements, and instructions for updating the SOM power sequencing firmware, please read the document **PicoZed SDR Z7035-AD9361 Power Sequencer Update** on the PicoZed SDR documentation page:

- <http://picozed.org/support/documentation/10511>

For any questions regarding this PCN please use the [PicoZed SDR Forum](#).