Combining the Xilinx Zynq®-7000 All-Programmable SoC (ARM®
dual-core Cortex™-A9 + 28 nm programmable logic) with the
Analog Devices AD-FMCOMMS3-EBZ FMC module featuring the
AD9361 integrated RF Agile Transceiver, the kit enables a broad
range of transceiver applications for wireless communications.
Tuned to a narrower RF range in the 2400 – 2500 MHz region,
the kit is ideal for the RF engineer seeking optimized system
performance meeting datasheet specifications in a defined range
of RF spectrum.

FEATURES
- Combines Avnet ZedBoard™ with Analog Devices
  AD9361 integrated RF Agile Transceiver
- RF 2 × 2 transceiver with integrated 12-bit DACs and ADCs
- Band: factory-tuned for optimal performance at 2400 – 2500 MHz
- User-modifiable to other RF center-frequencies from 70 MHz to 6.0 GHz
- Tunable channel bandwidth at the data converter: <200 kHz to 56 MHz
- Powered from single FMC connector
- Supports MIMO radio, with less than 1 sample sync on both ADC and DAC
- Includes schematics, layout, BOM, HDL, Linux drivers and application software
- Supports add on cards for spectrum specific designs (PA, LNA etc)
- SPI access for all device registers
- Optional MathWorks tools support for communications system design

TARGET APPLICATIONS
- Wireless communications development
- Remote radio head
- Software-defined radio
- Satellite modems
- Test and measurement equipment
- Radar and advanced imaging
- General-purpose data acquisition

To purchase this kit, visit www.zedboard.org
**FEATURED MANUFACTURERS**

**PARTS**

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Description</th>
<th>Resale</th>
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<tbody>
<tr>
<td>AES-ZSDR2-ADI-G</td>
<td>Zynq-7000 AP SoC / AD9361 Software-Defined Radio Evaluation Kit</td>
<td>$1,495 USD</td>
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<tr>
<td>AES-ZSDR-ADI-G-MATW-ANUL</td>
<td>MathWorks Software-Defined Radio (SDR) Design Package for Avnet Zynq</td>
<td>$14,600 USD</td>
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<td>AES-ZSDR-ADI-G-MATW-PERP</td>
<td>MathWorks Software-Defined Radio (SDR) Design Package for Avnet Zynq</td>
<td>$36,500 USD</td>
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